

A MODIFIED POWER FLOW METHOD FOR NON SYNCHRONOUS NETWORKS INTERCONNECTION

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ABSTRACT

This paper presents an efficient method for AC-DC power flow used in non- synchronous interconnection. The proposed method treats the converter as voltage dependent loads and the variables are eliminated from the power flow equations. The method is successfully implemented to IEEE 5-bus test system interconnected with IEEE 14-bus test system. The technical conditions for interconnecting power systems, the short circuit capacity of AC connected bus, the geometrical topology of the land in DC link path, and location of AC bus from the generating stations, are considered.

1. INTRODUCTION

HVDC transmission is now an acceptable alternative way to AC transmission, and is proving an economical solution not only for very long distance but also for underground cable and submarine transmission. It also a suitable mean of interconnecting systems of different frequencies or with problems of stability or fault capacity.

power flow analysis is an essential component of system studies carried out for planning, design and operation of power systems. It is basically a simulation of steady-state load and determines the operating point, which is later used for initializing variables in transient and dynamic system analysis. The basic load flow has to be substantially modified to be capable of modeling the operating state of the combined AC and DC system under the specified load conditions, generation and DC system control strategies.

Traditionally, two different approaches have been used to solve the power flow equations for AC-DC systems. The first approach is the unified method

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[1,2,3, &4], which gives full recognition to the interdependence between AC and DC system equations and simultaneously solve the complete set of equations. The solution vector is extended with the DC-variables. Also, the Jacobian matrix will be modified to include the DC system effect. The drawback of the unified method is that it is complex to program and difficult to combine with developments in AC power flow solution techniques, such as the fast decoupled method. The other approach is the sequential method [2,4], in which the AC and DC equations are solved separately and thus the integration into existing load-flow programs is carried out without significant modification or restructuring of the AC solution technique. For the AC iterations, each converter is modified simply by the equivalent real or reactive power injection at the terminal busbar. The terminal busbar voltages obtained from the AC iteration are then used to solve the DC equations and consequently, new power injections are obtained. This process continues iteratively to convergence. The sequential method is easy to implement, but convergence problems may occur at certain modes of operations. The new method used for AC-DC power flow, which is explained in [5] is used here to overcome the above difficulties, and can be used for interconnecting of electrical power system.

2. SYSTEM MODELS

2.1 DC SYSTEM MODELS

The objective of HVDC system model is to develop a generic bipolar or monopolar HVDC model [4,5, &6], which represents multi-terminal and multi-converter DC systems integrated with the AC system. The model must also be able to the more complex control Actions that are invariably associated with system configurations. The model of monopolar configuration is illustrated in Equs (1-9):

$$V_{dR} = \frac{3\sqrt{2}}{\pi} \cdot a_R \cdot V_{tR} \cdot \cos\alpha_R - \frac{3}{\pi} \cdot X_C \cdot I_d \quad (1)$$

$$P_{dR} = V_{dR} \cdot I_d \quad (2)$$

$$S_{dR} = \frac{3\sqrt{2}}{\pi} \cdot k \cdot a_R \cdot V_{tR} \cdot I_d \quad (3)$$

$$Q_{dR} = \sqrt{S_{dR}^2 - P_{dR}^2} \quad (4)$$

$$V_{dR} = V_{dI} + I_d \cdot R_d \quad (5)$$

$$V_{dl} = \frac{3\sqrt{2}}{\pi} \cdot a_1 \cdot V_{il} \cdot \cos\gamma_1 - \frac{3}{\pi} \cdot X_C \cdot I_d \quad (6)$$

$$P_{dl} = V_{dl} \cdot I_d \quad (7)$$

$$S_{dl} = \frac{3\sqrt{2}}{\pi} \cdot k \cdot a_1 \cdot V_{il} \cdot I_d \quad (8)$$

$$Q_{dl} = \sqrt{S_{dl}^2 - P_{dl}^2} \quad (9)$$

where

V_{dR} , V_{dl} Voltage of DC side of converter and inverter.

V_{iR} , V_{il} Voltage of converter and inverter AC bus.

a_R , a_I Converter and inverter transformer tap ratio.

I_d DC current.

α_R Converter firing angle.

α_I Inverter firing angle.

X_C Commutation reactance.

R_d DC line resistance.

K is a constant very close to unity, and equal to 0.995 for power flow analysis [2,4, &5].

2.2 AC-DC Power Flow Equations

The basic load flow has to be substantially modified to be capable of modelling the operating state of the AC and DC systems under specified conditions of load, generation and DC system control strategies.

The power mismatches for all AC buses can be calculated using the Equ (4):

$$\Delta P(V, \theta) = P^{spec} - P(V, \theta) \quad (10)$$

$$\Delta Q(V, \theta) = Q^{spec} - Q(V, \theta) \quad (11)$$

When the DC link is included in the power flow equations, only the mismatch equations at the converter and inverter terminal buses have to be modified as [4,5],

$$\Delta P_{iR} = P_{iR}^{spec} - P_{iR}^{spec}(V, \theta) - P_{dR}(V_{iR}, V_{il}, X_{dc}) \quad (12)$$

$$\Delta P_{il} = P_{il}^{spec} - P_{il}^{spec}(V, \theta) + P_{dl}(V_{iR}, V_{il}, X_{dc}) \quad (13)$$

$$\Delta Q_{iR} = Q_{iR}^{spec} - Q_{iR}^{spec}(V, \theta) + Q_{dR}(V_{iR}, V_{il}, X_{dc}) \quad (14)$$

$$\Delta Q_{dl} = Q_{dl}^{spec} - Q_{dl}^{spec}(V, \theta) + Q_{dl}(V_{IR}, V_{II}, X_{dc}) \quad (15)$$

where X_{dc} is a vector of internal DC-variables.

The DC- variables must satisfy

$$R(V_{IR}, V_{II}, X_{dc}) = 0 \quad (16)$$

The of internal DC- variables are given as,

$$X_{dc} = [\alpha_R \ \gamma_I \ a_R \ a_I \ V_{dR} \ V_{dI} \ I_d \ \phi_{IR} \ \phi_{II}]^T \quad (17)$$

where

- $p(v, \theta), Q(v, \theta)$ the injected active and reactive power at AC buses.
- V, θ vector of AC bus voltage magnitude and phase angle.
- P^{spec}, Q^{spec} specified active and reactive power at AC buses.
- $\Delta P, \Delta Q$ vector of active and reactive power mismatch
- P_t^{spec}, Q_t^{spec} specified real and reactive power at converter buses.
- P_t^{ac}, Q_t^{ac} real and reactive power transmitted by the AC network.

The AC-DC load flow problem may therefore be given in the following form using Newton-Raphson method using proposed method,

$$\begin{bmatrix} \Delta P \\ \Delta Q \end{bmatrix} = \begin{bmatrix} H & N' \\ J & L' \end{bmatrix} \begin{bmatrix} \Delta \theta \\ \Delta V/V \end{bmatrix} \quad (18)$$

Where

$$N'(IR, IR) = V_{IR} \frac{\partial P_{IR}^{ac}}{\partial V_{IR}} + V_{IR} \frac{\partial P_{dR}(V_R, V_{IR})}{\partial V_{IR}}$$

$$N'(IR, II) = V_{II} \frac{\partial P_{IR}^{ac}}{\partial V_{II}} + V_{II} \frac{\partial P_{dR}(V_{IR}, V_{II})}{\partial V_{II}}$$

$$N'(II, IR) = V_{IR} \frac{\partial P_{II}^{ac}}{\partial V_{IR}} - V_{IR} \frac{\partial P_{dI}(V_{IR}, V_{II})}{\partial V_{IR}}$$

$$N'(II, II) = V_{II} \frac{\partial P_{II}^{ac}}{\partial V_{II}} - V_{II} \frac{\partial P_{dI}(V_{II}, V_{II})}{\partial V_{II}}$$

The sub-matrix L' can be obtained in analogous method similar to N' . Thus in the proposed solution method, four mismatch equations and up to eight elements of the Jacobian have to be modified, but no new variables are added to the solution vector, when a DC link is included in the power flow. The partial derivatives are those required by Equ (18); $\partial P_{dR}(V_{IR}, V_{II})/\partial V_{IR}$, for example, is the derivatives of P_{dR} w.r.t. V_{IR} , assuming V_{II} is kept constant. The DC variables, however, are not kept constant as opposed to $\partial P_{dR}(V_{IR}, V_{II}, \bar{X})/\partial V_{IR}$, which is used in Equ (18).

The presented method overcomes the major drawbacks of the other methods as it treats the real and reactive powers consumed by the converters as the

voltage dependent loads. The DC equations are solved analytically, and the DC variables are eliminated from the power flow equations.

3. NON-SYNCHRONOUS INTERCONNECTION

In order to solve the power flow through non-synchronous networks connected by HVDC transmission, two AC systems have to be handled simultaneously, i.e. two slack bases and two angle reference are needed. If the DC link connects two large networks, the inclusion of the DC link in power flow increases the number of variables. On the other hand, if the DC link feeds power from a large network to a weak island network, convergence problem for the island network can slow down in the convergence of the whole system. Thus a procedure to decouple the AC networks in power flow is desirable. The networks are coupled via the voltage magnitudes at the converter terminal AC buses, as shown in Equ (19):

$$\begin{bmatrix} \Delta P_1 \\ \Delta P_{IR1} \\ \Delta Q_1 \\ \Delta Q_{IR} \\ \Delta P_2 \\ \Delta P_{IR2} \\ \Delta Q_2 \\ \Delta Q_{II} \end{bmatrix} = \begin{bmatrix} H & N'_1 & 0 & 0 & 0 \\ & J_1 & L'_1 & 0 & 0 \\ & 0 & 0 & H & N'_2 \\ & 0 & 0 & N'_{2R} & L'_2 \\ & 0 & 0 & L'_{2R} & J_2 \end{bmatrix} \begin{bmatrix} \Delta \theta_1 \\ \Delta \theta_{IR} \\ \Delta V_1 \\ \Delta V_{IR} \\ \Delta \theta_2 \\ \Delta \theta_{II} \\ \Delta V_2 \\ \Delta V_{II} \end{bmatrix} \quad (19)$$

where

$$N'_{II} = -\frac{\partial \Delta P_{IR}}{\partial V_{II}} = \frac{\partial \Delta P_{dR}}{\partial V_{II}}$$

Table (1) Short Circuit Capacity of IEEE 14-Bus Test System

Bus No.	1	2	3	4	5	6	7	8
SCL(MVA)	1266	1724	806	1254	1205	1078	749	366
Bus No.	9	10	11	12	13	14		
SCL(MVA)	763	584	538	635	809	409		

Table (2) Short Circuit Capacity of IEEE 5-Bus Test System

Bus No.	1	2	3	4	5
SCL(MVA)	1286	1810	1540	1513	879

The only coupling between the systems are through the cross derivatives. If both the real and reactive power at the inverter end independent of the AC

voltage at the rectifier end, N_{2R} and L'_{2R} will both be zero and the power flow for the AC system on the inverter side can be solved independently of the AC system on the rectifier side. Also, once the AC voltage on the inverter side is known, the power flow on the rectifier side can be solved using the known value of V_H .

4. TEST RESULTS AND DISCUSSIONS

To decide the location of DC link, the following conditions must be taken into considerations:

- The geometrical topology of the land in DC link path,
- The short circuit capacity of AC connected bus, and
- location of AC bus from the generating stations.

The converter requires a high reactive power consumption in commutation process. So, the converter bus must be a very strong bus with high short circuit capacity.

The IEEE 14-bus test system is connected with IEEE 5-bus test system to examine the interconnection between the two networks using an HVDC link. From results the rectifier must be connected with 5-bus test system and the inverter station must be connected with 14-bus test system. This is because the 5-bus test system is more strong as compared with the 14-bus test system as illustrated from short circuit capacity of all buses of the two systems. Tables (1) & (2) illustrate the short circuit capacity of 14-bus system and 5-bus system respectively. The voltage profile of the two test systems with different locations of DC link are illustrated in Figure (1) & (2) for 14-bus system and 5-bus system respectively.

Tables (3) & (4) illustrate the active and reactive power losses (as a percentage of total active power demand) for different positions of interconnection between the two systems.

It is found that, the best location for DC link when rectifier connected with bus 2 (5-bus system) and inverter with bus 4 (14-bus system). This choice fulfills the technical conditions mentioned previously.

Table (3) percentage active power losses for different positions of DC link between the two systems

connected busses		percent active
5-bus system	14-bus system	power losses
2	4	6.63
2	9	6.71
2	14	6.91
3	4	7.28
3	9	7.37
3	14	7.57
5	4	7.82
5	9	7.83
5	14	8.03

Table (4) percentage reactive power losses for different positions of DC link between the two systems

connected busses		percent reactive
5-bus system	14-bus system	power losses
2	4	34.49
2	9	35.15
2	14	35.27
3	4	37.49
3	9	36.82
3	14	37.60
5	4	38.36
5	9	38.37
5	14	39.15

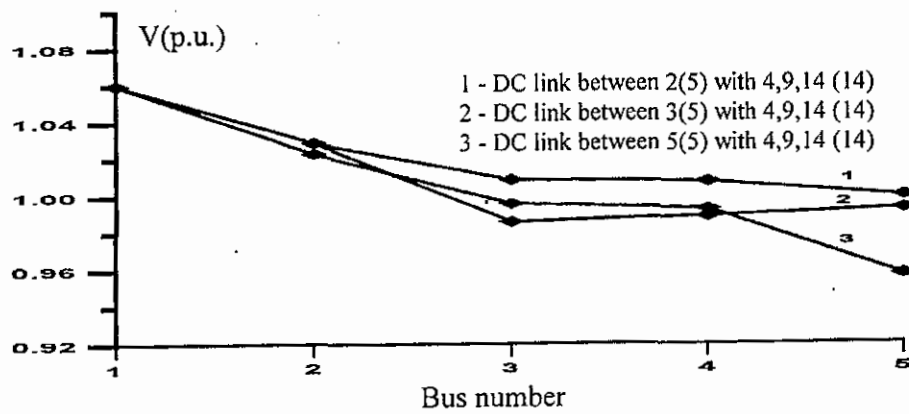


Figure (3) voltage profile of 5-bus system for different positions of connection between the two systems

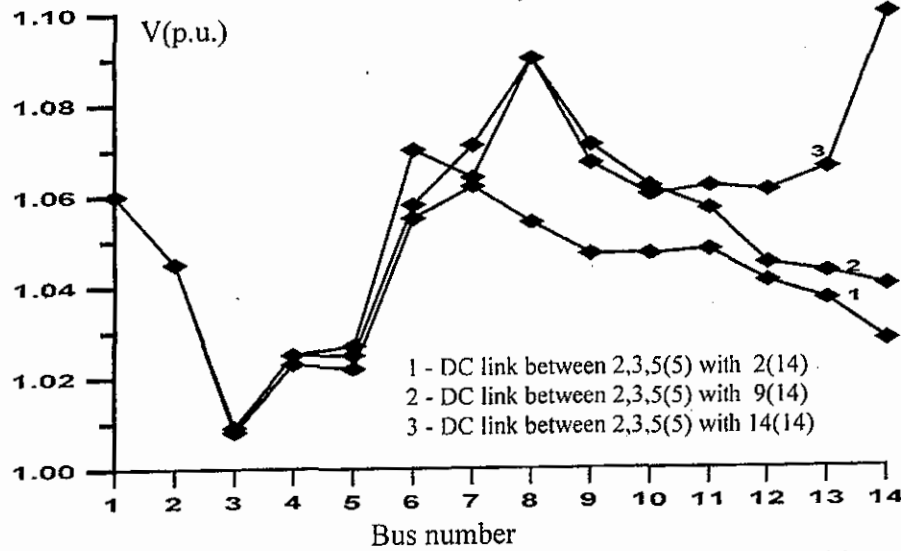


Figure (4) voltage profile of 14-bus system for different positions of connection between the two systems

5. CONCLUSION

The non-synchronous interconnection between two AC systems has a difficulty of two slack buses and two references for power flow problem. The proposed method is modified to handle the concept of networks interconnection, which allows the power flow for two AC networks in non synchronous interconnection to be solved independently. The modified method is successfully utilized to interconnect between IEEE 5-bus test system and IEEE 14-bus test system to determine the best location of the DC link taking into account all technical considerations.

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الطريقة المعدلة لدراسة سريان القدرة بين الشبكات المرتبطة ترابط غير متزامن

يقدم هذا البحث طريقة مثالية لعملية سريان القدرة الكهربائية تستخدم لدراسة سريان القدرة للشبكات المرتبطة (ذات تردد مختلف) من خلال خط ربط غير متزامن وتعالج هذه الطريقة المعدل على أنه حمل يعتمد على الجهد مع حذف متغيرات النظام المستمر من معادلات سريان القدرة الكهربائية ، وقد تم تطبيق هذه الطريقة للربط بين نظامين من النوع القياسي حيث يتكون الأول من خمس نقاط توزيع (IEEE 5-Bus Test System) بينما يتكون الآخر من أربعة عشر نقطة توزيع (IEEE 14-Bus Test System) مع أخذ الشروط الفنية لعملية الربط في الاعتبار وهذه الشروط هي قرب نقطة الربط من مناطق التوليد ، أن يكون مسار الربط مناسباً جغرافياً، وأن يكون مستوى القصر لنقطة الربط عالى.